

Notice of References Cited	Application/Control No. 10/694,284	Applicant(s)/Patent Under Reexamination XU ET AL.	
	Examiner Fernando L. Toledo	Art Unit 2823	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-2005/0118749 A1	06-2005	Sakamoto et al.	438/107
	B	US-2004/0256764 A1	12-2004	Choi et al.	264/293
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
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	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf and Tauber; Silicon Processing for the VLSI Era Volume 1: Process Technology; 1986; Lattice Press, Sunset Beach, CA; pp. 541, 542, 544, 564 and 565
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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